WEICHEN YU

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EDUCATION

University of Chinese Academy of Sciences, Beijing, PRC *09/2020 - present*
Master of Science in Artificial Intelligence GPA 3.84/4.0

University of Chinese Academy of Sciences, Beijing, PRC *09/2016 - 07/2020*
Bachelor of Science in Electrical Engineering GPA 3.67/4.0

University of California, Berkeley, California, US *01/2019 - 09/2019*
Visiting Student in Electrical Engineering and Computer Sciences GPA 3.67/4.0

TOEFL: 109: 29(L)+29(R)+28(W)+23(S) GRE: 157(V)+170(M)+3.5

PUBLICATION

- **Weichen Yu**, Hongyuan Yu, Yan Huang, Qiang Liu, Liang Wang. Backdoor Intervention for Noisy Labels Learning on Biased Background Knowledge. (plan to submit to ICLR 2023).

- **Weichen Yu**, Hongyuan Yu, Yan Huang, Chunshui Cao, Liang Wang. CNTN: Cyclic Noise-Tolerant Network. (under review)

- **Weichen Yu**, Hongyuan Yu, Yan Huang, Liang Wang. Generalized Inter-class Loss for Gait Recognition. ACMMM 2022 (Poster).

- Hongyuan Yu*, Ting Li*, **Weichen Yu***, Yan Huang, Jianguo Li, Liang Wang, Andy Liu. Regularized Graph Structure Learning with Explicit and Implicit Knowledge for Multi-variate Time-Series Forecasting. IJCAI 2022 (Oral)

- DOI:10.13234/j.issn.2095-2805.2020.4.38 Han C, **Weichen Y**, Xiaoguang C, Puqi N, Xuhui W. Genetic Algorithm Based SiC MOSFET On-state Resistance Modeling Method [J].Journal of Power Supply, 2020, 18(04): 38-44.

CHALLENGE

- Learning and Mining with Noisy Labels Challenge, IJCAI-ECAI 2022 2rd place
- Visual Language Navigation Contest. 2rd place

AWARDS & HONORS

Huawei Mathmatical Contest in Modeling, First Prize of UCAS *10/2018*
UCAS Student Scholarship *09/2019,2018,2017*

SELECTED PROJECTS

Power supply noise analysis using autocorrelation *01/2019-09/2019*
Advisor: Prof. Seth R. Sanders University of California, Berkeley, EECS

- developed better algorithms which combined autocorrelation with averaging on Simulink to analyze power supply noise and achieve high resolution (1 mV) at high frequency (30GHz).

- Significantly reduced the cost from 500$ to 20$ and improved the performance and resolution by accumulating small steps of advancement such as the step of float-point to fixed-point optimization.

TECHNICAL STRENGTHS

Programming Languages *Python, C, Matlab, Verilog, Lisp, TeX*
Software *Multisim, Advanced Design System, Vivado, Keil, Cadence, OrCAD, Phonopy*